

PTH03030

3.3 Vin Single Output

Data Sheet

Total Power: 75 Watts
Input Voltage: 2.95 - 3.65 Vdc
of Outputs: Single

SPECIAL FEATURES

- 30 A output current
- 3.3 V input voltage
- Wide-output voltage adjust (0.8 V - 2.5 V)
- Auto-track™ sequencing*
- Margin up/down controls
- Pre-bias start-up capability
- Efficiencies up to 93%
- Output ON/OFF inhibit
- Output voltage sense
- Point-of-Load-Alliance (POLA) compatible
- RoHS compliant
- Two year warranty

SAFETY

- UL/cUL CAN/CSA-C22.2 No. 60950-1-03
- UL 60950-1 File No. E174104
- TÜV Product Service (EN60950) Certificate No. B04 06 38572 044
- CB report and certificate to IEC60950, Certificate No. US/8292/UL



Electrical Specifications

Input		
Input voltage range	(See Note 3)	2.95 - 3.65 V
Input current	No load	10 mA typical
Remote ON/OFF	(See Note 1)	Positive logic
Start-up time		1 V/ms
Undervoltage lockout		2.8 - 2.95 V typical
Track input voltage	Pin 11 (See Note 6, 7)	±0.3 Vin
Output		
Voltage adjustability	(See Note 4)	0.8 - 2.5 Vdc
Setpoint accuracy		±2.0% Vo
Line regulation		±10 mV typical
Load regulation		±12 mV typical
Total regulation		±3.0% Vo
Minimum load		0 A
Ripple and noise	20 MHz bandwidth	30 mV pk-pk
Temperature co-efficient	-40 °C to +85 °C	±0.5% Vo
Transient response	(See Note 5)	70 µs recovery time Overshoot/undershoot 100 mV
Margin adjustment		±5.0% Vo

All specifications are typical at nominal input, full load at 25 °C unless otherwise stated. Cin = 1500 µF, Cout = 0 µF.

*Auto-track is a trademark of Texas Instruments.

General Specifications

Efficiency	(See Efficiency Table)	93% max.
Insulation voltage		Non-isolated
Switching frequency	Fixed	275 - 325 kHz
Approvals and standards		EN60950, UL/cUL60950
Material flammability		UL94V-0
Dimensions	L x W x H	34.80 x 28.45 x 9.00 mm 1.370 x 1.120 x 0.354 in
Weight		10 g (0.35 oz)
MTBF	Telcordia SR-332	2,821,000 hours

EMC Characteristics

Electrostatic discharge	EN61000-4-2, IEC801-2
Conducted immunity	EN61000-4-6
Radiated immunity	EN61000-4-3

Environmental Specifications

Thermal performance (See Note 2)	Operating ambient temperature	-40 °C to +85 °C
	Non-operating temperature	-40 °C to +125 °C
MSL ('Z' suffix only)	JEDEC J-STD-020C	Level 3
Protection		
Short-circuit	Auto reset	45 A typical
Thermal		Yes

Ordering Information

Model Number ⁽⁹⁾	Output Power (Max.)	Input Voltage	Output Voltage	Output Current (Min.)	Output Current (Max.)	Efficiency (Typical)	Regulation	
							Line	Load
PTH03030	75 W	2.95 - 3.65 V	0.8 - 2.5 V	0 A	30 A	93%	±10 mV	±12 mV

Part Number System with Options

Product Family	Input Voltage	Output Current	Mechanical Package	Output Voltage Code	Pin Option	Mounting Options	Pin Option
PTH	03	03	0	W	A	S	T
Point-of-Load Alliance compatible	03 = 3.3 V	03 = 30 A	Always 0	W = Wide		D = Horizontal through-hole (Matte Sn) Z = Surface-mount (96.5/3.0/0.5 Sn/Ag/Cu pin solder material)	No Suffix = Trays T = Tape and Reel ⁽⁹⁾

Output Voltage Adjustment

The ultra-wide output voltage trim range offers major advantages to users who select the PTH03030. It is no longer necessary to purchase a variety of modules in order to cover different output voltages. The output voltage can be trimmed in a range of 0.8 Vdc to 2.5 Vdc. When the PTH03030 converter leaves the factory the output has been adjusted to the default voltage of 0.8 V.

Efficiency Table (I_o = 20A)

Output Voltage	Efficiency
V _o = 1.0 V	85%
V _o = 1.2 V	87%
V _o = 1.5 V	89%
V _o = 1.8 V	91%
V _o = 2.0 V	92%
V _o = 2.5 V	93%

Notes:

- Remote ON/OFF: Positive Logic
ON: Pin 3 open; or V > V_{in} - 0.5 V
OFF: Pin 3 GND; or V < 0.8 V (min - 0.2 V).
- See Figures 1 for safe operating curves.
- A 1500 µF electrolytic input capacitor is required for proper operation. The capacitor must be rated for a minimum of 900 mA rms of ripple current.
- An external output capacitor is not required for basic operation. Adding 330 µF of distributed capacitance at the load will improve the transient response.
- 1 A/µs load step, 50 to 100% I_{omax}, C_{out} = 330 µF.
- If utilized V_{out} will track applied voltage by ±0.3 V (up to V_o set point).
- The pre-bias start-up feature is not compatible with Auto-Track™. This is because when the module is under Auto-Track™ control, it is fully active and will sink current if the output voltage is below that of a back-feeding source. Therefore to ensure a pre-bias hold-off, one of the following two techniques must be followed when input power is first applied to the module. The Auto-Track™ function must either be disabled, or the module's output held off using the Inhibit pin. Refer to Application Note 152 for more details.
- Tape and reel packaging only available on the surface-mount versions.
- NOTICE: Some models do not support all options. Please contact your local Artesyn representative or use the on-line model number search tool at <http://www.artesyn.com> to find a suitable alternative.

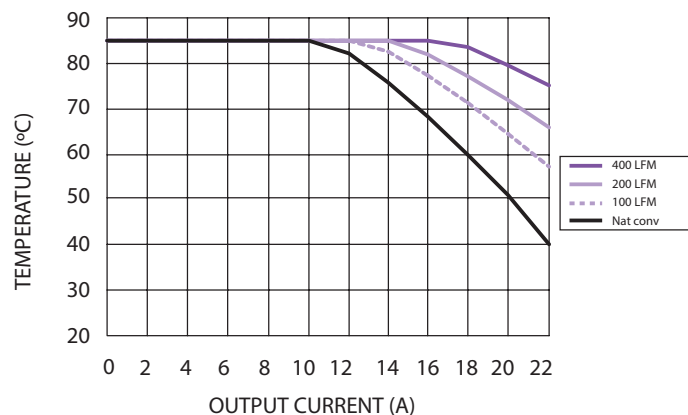


Figure 1 - Safe Operating Area
 $V_{in} = 3.3\text{ V}$, Output Voltage = 2.5 V (See Note A)

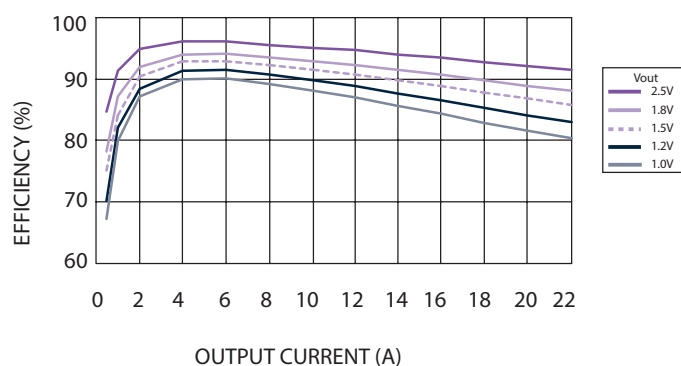


Figure 2 - Efficiency vs Load Current
 $V_{in} = 3.3\text{ V}$ (See Note B)

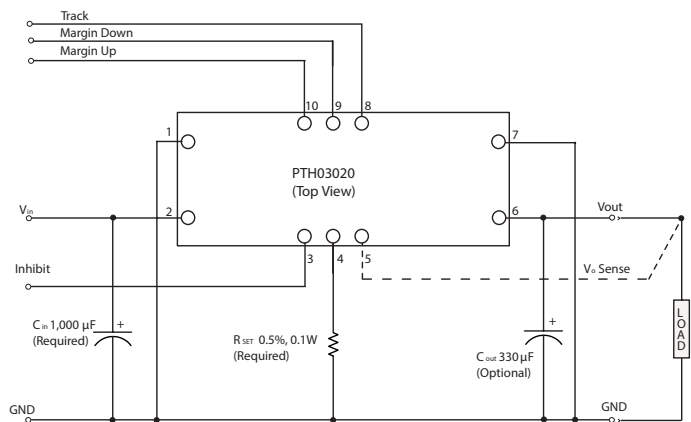


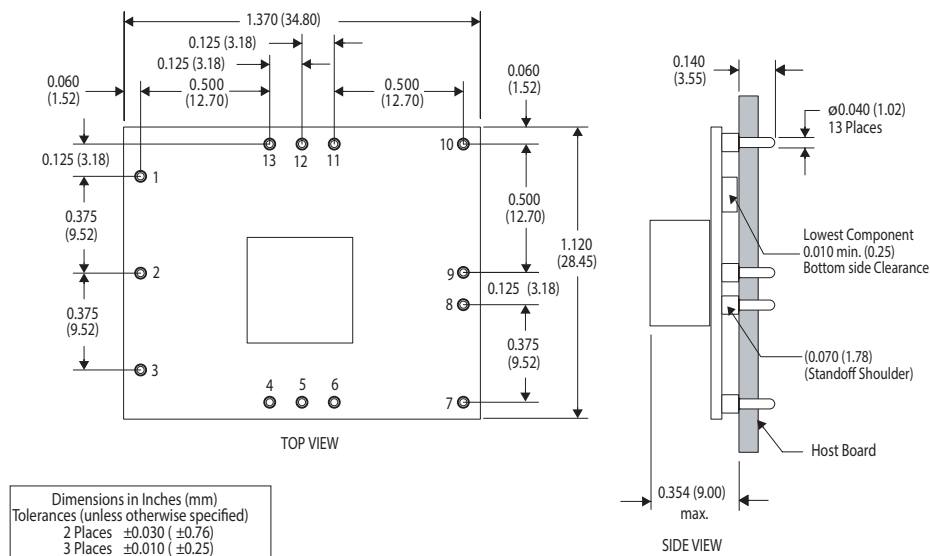
Figure 3 - Standard Application

Notes:

- SOA curves represent the conditions at which internal components are within the Artesyn derating guidelines.
- Characteristic data has been developed from actual products tested at 25 °C. This data is considered typical data for the converter.

Mechanical Drawings

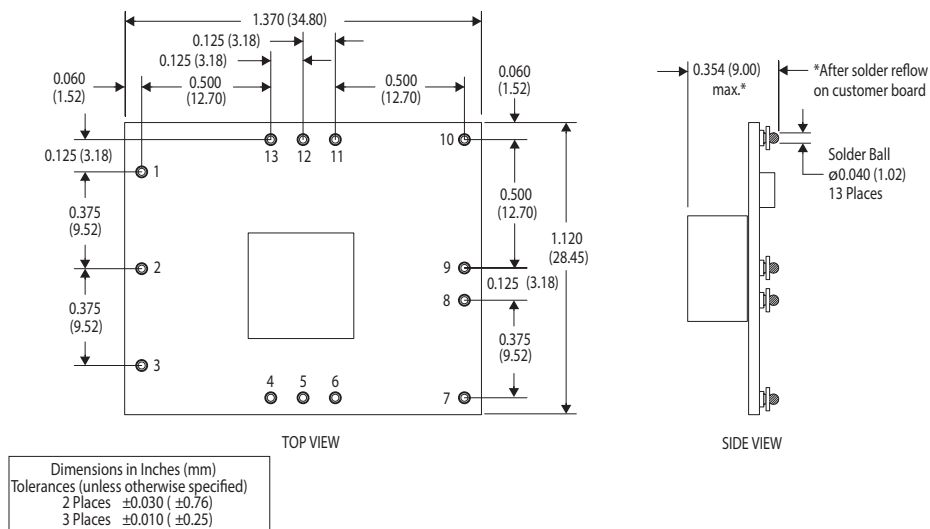
Plated through-hole



Pin Assignments	
Pin	Function
1	Ground
2	Vin
3	Ground
4	Inhibit*
5	Vo adjust
6	Vo sense
7	Ground
8	Vout
9	Vout
10	Ground
11	Track
12	Margin down*
13	Margin up*

*Denotes negative logic:
Open = Normal operation
Ground = Function active

Surface-mount



WORLDWIDE OFFICES

Americas

2900 South Diablo Way
Suite B100
Tempe, AZ 85282, USA
+1 888 412 7832

Europe (UK)

Ground Floor Offices, Barberry House
4 Harbour Buildings, Waterfront West
Brierley Hill, West Midlands
DY5 1LN, UK
+44 (0) 1384 842 211

Asia (HK)

14/F, Lu Plaza
2 Wing Yip Street
Kwun Tong, Kowloon
Hong Kong
+852 2176 3333

ARTESYN
An Advanced Energy Company

www.artesyn.com

Artesyn Embedded Technologies, Artesyn Embedded Power, Artesyn, and all Artesyn related logos are trademarks and service marks of Artesyn Embedded Technologies, Inc. All other names and logos referred to are trade names, trademarks, or registered trademarks of their respective owners. Specifications are subject to change without notice. © 2019 Artesyn Embedded Technologies, Inc. All rights reserved. For full legal terms and conditions, please visit www.artesyn.com/legal.

For more information: www.artesyn.com
For support: productsupport.ep@artesyn.com